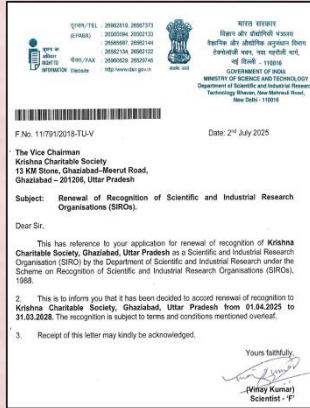


## DEPARTMENTAL HIGHLIGHTS

### Scientific and Industrial Research Organizations (SIROs) Recognition

KIET Group of Institutions, Delhi-NCR, Ghaziabad (UP), under Krishna Charitable Society recognized as a Scientific and Industrial Research Organisations (SIROs) by the Department of Scientific and Industrial Research (DSIR), Ministry of Science and Technology, Government of India. This prestigious recognition help KIET to establish in house Research and Development on new technologies, products, processes etc. It also provides a link between scientific laboratories and industrial establishments for transfer of technologies through National Research Development Corporation (NRDC) and facilitates investment in R&D through Central Electronics Limited (CEL).



### Centre of Advanced VLSI Design (Powered by Cadence University Program)



The Centre of Advanced VLSI Design (Powered by Cadence University Program) at KIET Group of Institutions, Ghaziabad is a state-of-the-art facility dedicated to fostering innovation, research, and excellence in VLSI design. The centre has 30 computer system with advanced configuration and is equipped with the **Cadence University Research Bundle**. The Cadence University Research bundle consists of all the Front-end and Back-end tools required for chip design. The objectives of Centre of Advanced VLSI Design is to bridge the gap between academic learning and industry requirements by providing industry standard EDA tools and training in VLSI design, simulation, verification, and implementation.

### Robotics and Mechatronics Lab

Center of Excellence in Robotics and Mechatronics has been established by Department of ECE, KIET Group of Institutions. This specialized lab operates beyond curriculum and provides interdisciplinary hands-on learning opportunities for students across different domains. It houses advanced robotic platforms including Fire Bird V, VI, XII, 4-wheel mobile robots, and a **humanoid robot named Anushka** (in picture). Students from the Center actively participate in national-level robotics initiatives such as e-Yantra competitions supported by IIT Bombay, gaining experiential exposure, robotics kits, and notable achievements.



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**KIET**  
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Established: 1998

**Deemed to be University**

(Under Section 3 of the UGC Act, 1956)



**IEEE**  
UP SECTION (INDIA)

**IEEE ELECTRONICS PACKAGING SOCIETY**

### 6<sup>th</sup> International Conference

on

**Emerging VLSI and Semiconductor Technology for AI and Computing Applications 2026**

(EVST- 2026)

**Conference Record #69093**

**26<sup>th</sup> -27<sup>th</sup> June 2026**



**Organized by**

**Department of Electronics and Communication Engineering  
KIET Group of Institutions, Delhi-NCR, Ghaziabad**

(Recognized as a Scientific & Industrial Research Organisations (SIROs) by Department of Scientific and Industrial Research (DSIR), New Delhi and Rank Band (151-200) for Engineering in NIRF 2025.



## ABOUT KIET

Established in 1998 with an initial intake of 180 students, KIET has grown into a premier institution recognized for academic excellence and industry-aligned education. With 27 years of legacy in Engineering, Pharmacy, Management, and Computer Applications, KIET has continually strengthened its academic standards and infrastructure. **The Institute achieved Autonomous Status in 2024 and has been conferred Deemed-to-be-University status under Section 3 of the UGC Act, 1956 in November 2025.** The Institute now has 9300+ students and is empowered with 440+ highly qualified full-time faculty to nurture our students, with 100% Placement & Internship assistance. Consistency in maintaining academic results is one of the key strengths of KIET.

**The Institute is accredited with NAAC 'A+', and all eligible programs are NBA accredited.** In the **NIRF India Rankings 2025**, KIET secured **Rank 71 in Pharmacy, 151–200 band in Engineering, and 11–50 band in Innovation.** The Institute also boasts a **QS I-GAUGE Diamond rating** and recognition as a **Scientific and Industrial Research Organization (SIRO) by DSIR.** Our institute harbors in-house Entrepreneurship Cell, i.e. **Technology Business Incubator (TBI-KIET)**, Innovation Centre, Department of Public & International Relations, and a dedicated Placement Center (CRPC) that upskill and hone the talent of students, through various activities. KIET gives equal importance to curricular, co-curricular and extra-curricular activities. KIET regularly organizes a plethora of such activities to give a maximum opportunity to its students to groom into confident and strong personalities - enrich their learning experiences. KIET houses a robust ecosystem of Centers of Excellence, serving as catalysts for advanced learning, research, and innovation. These include **Supercomputing Centre of Excellence, Centre for Space Technologies, Advanced Woodworking Centre, Centre for Drone Technologies, Herbal Product Development Centre, Centre for E-Mobility Solutions, Wipro Centre of Excellence in Cyber security, Centre for Automotive Mechatronics, Centre for Advanced VLSI Design** etc.

## ABOUT THE DEPARTMENT

Department of ECE grooms the students to excel in the field of technology. Our students are trained in both software and hardware skills and basic inputs are provided to make them self-confident to work in industry and get encouragement for higher studies and research. The Department's motto is to upgrade skills and knowledge of our young engineers to enable them not only to survive but be the best in the present competitive world. The department also contributes to the society by accomplishing technical projects that cater to the various requirements of the present-day world. The students are also encouraged to participate in various technical and extracurricular events. The department has qualified and dedicated faculty members to provide good technical support to all the students. The department of ECE has a vision to become a centre of excellence in the field of Electronics and Communication Engineering. All our faculty and students are dedicated to achieve this goal with full vigor, enthusiasm and good ethical values. The Department is involved in high quality research on several domains like Optical Integrated Circuits, Signal Processing and Communication, Semiconductor Device Characterization and Integration, Advanced Microwave Techniques and other emerging fields in Electronics and Communication Engineering. To foster and enrich the same, **department has signed MoUs with various industries such as Nokia, ISRO, TI, etc.**

## ABOUT THE CONFERENCE (EVST-2026)

The **International Conference on Emerging VLSI and Semiconductor Technology for AI and Computing Applications 2026** is poised to be a significant gathering for professionals in the fields of **VLSI design, semiconductor technology, artificial intelligence, and computing systems.** The conference consists of various sessions, including keynotes and parallel sessions. Each session will be addressed by outstanding experts who will highlight recent advances in various facets of Emerging VLSI and Semiconductor Technology. The conference will offer the budding researchers with an opportunity to present their work in front of eminent experts in their respective fields. Discussions on the latest innovations, trends, practical concerns and challenges faced in these fields will take place. Innovation will bring added value and widen the employment base. The present conference aims to establish robust, mutual interactions and cooperation with scientific educational, doctors, governmental, non-governmental organizations, industries across local, national and international levels. Its objective is to create as many meaningful connections as possible to enable scientific and industrial exchange, to strengthen research collaborations with scientific centers across the country and around the world.

## PARTICIPATION

The Conference is aimed to felicitating the exchange of ideas and concepts among a broad group that includes academicians, students, researchers, innovators and industry.

## CONFERENCE TRACKS

### Category-1: Emerging Materials and Devices Technologies

- 2-D Material
- High-voltage MOSFET
- Si photonics and optoelectronics Spintronics and quantum materials
- MEMS/NEMS
- Organic electronics
- Emerging memory technologies.

### Category-2: Embedded Systems Design

- Generative AI Applications in Embedded and Machine Learning
- Real-Time Systems and Applications
- Embedded Software and Firmware Development Hardware/Software Co-Design
- IoT, Edge, and Wireless Embedded Systems
- Embedded Vision and Multimedia Processing
- Reconfigurable Systems and FPGAs
- Automotive, Healthcare, and Industrial Applications
- Advanced Robotics

### Category-3: VLSI Circuit and SoC Design

- Digital, Analog, Mixed signal, RF circuits
- RTL design
- Processor Architecture
- Hardware accelerators
- FPGA based design
- Quantum circuits
- Neuromorphic
- Memory
- Circuits, Accelerators, SoC for ML/AI applications

### Category-4 Packaging and Manufacturing

- Manufacturing technologies
- Packaging
- Signal and Power Integrity

### Category-5 Electronics Design Automation, Testing and Verification

- CAD Tools, software
- CAD for FPGA
- High Level Synthesis
- System software
- Testing and Verification

### Category-6 Emerging Technologies in Electronics & Communication

- Industry 5.0 – Human-Machine Collaboration
- Quantum Computing and Communication
- 6G Wireless Systems & NGN
- Green Technologies based on SDGs

## GUIDELINES FOR AUTHORS

Soft copy of the full length paper in MS word/pdf format (as per the conference template) must be submitted before **28<sup>th</sup> February 2026**. All the submitted full-length papers will be reviewed and evaluated based on the originality, technical aspects, research depth, authenticity of the data used and relevance to the theme of the conference. The work should be original and must not have been published in any Journal or proceedings. **All submissions must adhere to a plagiarism limit of less than 10%.**

All authors are required to submit unpublished full-length research papers to Microsoft CMT platform:

<https://cmt3.research.microsoft.com/EVST2026>

## PUBLICATION AND INDEXING

Accepted and presented papers will be submitted for inclusion in the IEEE Xplore digital library, subject to meeting IEEE Xplore's scope and quality requirements.

## IMPORTANT DATES

Paper Submission deadline	Feb 28, 2026 (IST)
Notification of Acceptance	March 30, 2026 (IST)
Camera-ready paper submission & Registration deadline	April 30, 2026 (IST)
IEEE EVST 2026 Conference Date	June 26-27, 2026 (IST)

## REGISTRATION FEES

- |                                 |          |
|---------------------------------|----------|
| • Non-IEEE Professionals        | INR 8500 |
| • IEEE Professional Members     | INR 7000 |
| • Non-IEEE Students             | INR 5500 |
| • IEEE Student Members          | INR 4500 |
| • Industry Participants         | INR 8000 |
| • Non-IEEE Foreign Participants | USD 300  |
| • IEEE Foreign Participants     | USD 250  |

### \* Note

- ❑ All papers are required to be prepared and submitted in the IEEE Standard two – column conference format of A4 size in English. Papers must be four to six pages in length, including texts, Figures, photographs and references.
- ❑ Detailed format information is available in IEEE Website. <https://www.ieee.org/conferences/publishing/templates>



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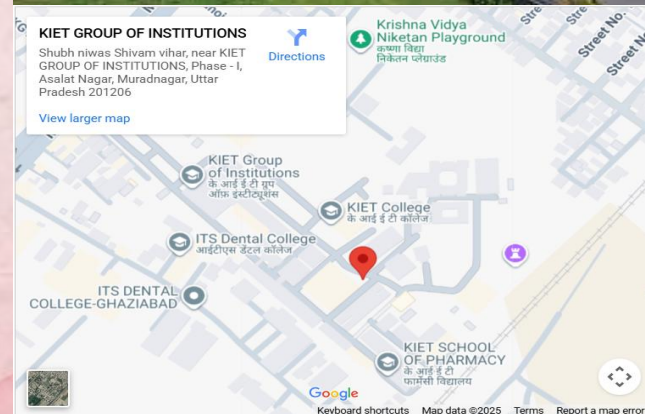
## BEST PAPER AWARDS

The "Best Paper Awards" will be given for each track of conference, selected by the committee among the presented papers in EVST 2026.

**1st Best Paper Award** **Rs. 4,000/- Cash Prize with goodies**

**2nd Best Paper Award** **Rs. 2,000/- Cash Prize with goodies**

## Location Map (KIET)



<https://maps.app.goo.gl/ickxqdSHiXHKWW13A>

**Department of Electronics & Communication Engineering****6<sup>th</sup> International Conference  
on****Emerging VLSI & Semiconductor Technology  
for AI and Computing Applications  
(EVST-2026)****26<sup>th</sup> - 27<sup>th</sup> June 2026****Sponsored by: IEEE UP Section  
Conference Record #69093****Conference Tracks**

- Emerging Materials & Devices** - 2-D Materials, High-voltage MOSFET, Si Photonics and optoelectronics, Spintronics, MEMS/NEMS, Organic Electronics, Emerging Memory Technologies.
- Embedded Systems Design** - Generative AI Applications for Embedded and ML, Real-Time Systems, Embedded Software and Firmware Development Co-Design, IoT, Edge Computing, Wireless Embedded System, Embedded Vision and Multimedia Processing, Reconfigurable Systems and FPGAs, Automotive, Health Care and Industrial Applications, Advanced Robotics.
- VLSI Circuit & SoC Design** - Digital/Analog/Mixed/RF Circuits, RTL Design, Processor Architectures, FPGA-based Design, Hardware Accelerators, Neuromorphic Memory, Quantum Circuits, Accelerators, AI/ML SoCs.
- Packaging & Manufacturing** - Manufacturing Technologies, Packaging, Signal & Power Integrity.
- EDA, Testing & Verification** - CAD Tools and Software, CAD for FPGA, High-Level Synthesis, System Software, Testing & Verification.
- Emerging Technologies in Electronics & Communication** - Industry 5.0 Machine Human Collaboration, Quantum Computing & Communication, 6G Wireless Systems and NGN, Green Technologies based on SDG.

**Call for Papers**

Accepted and presented papers will be submitted for inclusion in the IEEE Xplore digital library, subject to meeting IEEE Xplore's scope and quality requirements.

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All submission must adhere to a plagiarism limit of less than 10%.

**Important Dates****Paper Submission Deadline:****28<sup>th</sup> February 2026****Acceptance Notification:****30<sup>th</sup> March 2026****Camera-Ready Paper Submission &  
Registration Deadline: 30<sup>th</sup> April 2026****IEEE EVST-2026 Conference Dates:****26<sup>th</sup> - 27<sup>th</sup> June 2026****Registration Fees****Non-IEEE Professionals: ₹8,500****IEEE Professional Members: ₹7,000****Non-IEEE Students: ₹5,500****IEEE Student Members: ₹4,500****Industry Participants: ₹8,000****Foreign Participants (Non-IEEE/IEEE):  
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